Notice of Allowability	Application No.	Applicant(s)	
	10/605,765	YAMAZAKI ET AL.	
	Examiner	Art Unit	
	Dang T Nguyen	2824	
The MAILING DATE of this communication appeals all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comme GHTS. This application is	n this application. If not included nunication will be mailed in due cou	ırse. THIS
1. This communication is responsive to 24 October 2003.			
2. X The allowed claim(s) is/are 1-16.			
3. $igotimes$ The drawings filed on <u>24 October 2003</u> are accepted by the	e Examiner.		
 4. Acknowledgment is made of a claim for foreign priority unally all b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). 	been received. been received in Applicati	on No	from the
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submitted by the Company of th	IENT of this application. itted. Note the attached EX	AMINER'S AMENDMENT or NOT	
INFORMAL PATENT APPLICATION (PTO-152) which give	, , ,	or decraration is deficient.	
6. CORRECTED DRAWINGS (as "replacement sheets") mus		/ DTO 040) -#	
(a) including changes required by the Notice of Draftspers		w (PTO-948) attached	
 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's 		or in the Office action of	
Paper No./Mail Date	s Amendment / Comment t	of in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on he header according to 37 C	the drawings in the front (not the bac FR 1.121(d).	ck) of
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			∍ the
Attachment(s)			
1. ☑ Notice of References Cited (PTO-892)	<u> </u>	nformal Patent Application (PTO-1	52)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		Summary (PTO-413), ./Mail Date .	
 Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 10/24/03 & 11/3/03 Examiner's Comment Regarding Requirement for Deposit 	08), 7. ☐ Examiner's	s Amendment/Comment s Statement of Reasons for Allowa	nce
of Biological Material	9. ⊠ Other <u>Sea</u>		.,
		la lun	
		Vu A. Le	
		Primary Examiner	

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DETAILED ACTION

- 1. This office action is in response to the following communications: The Application filed on 10/24/03 and the Information Disclosure Statement filed on Oct. 24, 2003 and Nov. 03, 2003.
- 2. Claims 1 16 are pending on this application. Claims 1, 4, 5, 9 and 13 are independent claims.

Allowable Subject Matter

- 3. Claims 1- 16 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

With respect to claims 1, 4, 5, 9 and 13, in addition to other elements in the respective claim, the prior art does not teach a memory system comprising: when power supply to the memory module is started and initialization of the memory module begins, the output switching means selects output from the ID information output circuit before the memory module is initially written. Therefore, if reading is performed at any address before the initial writing is done, ID information for the memory module will be read out from the ID information output circuit.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Farmwald et al. (U.S. Patent No. 6,070,222) discloses a memory device having identification register to store an identification code to identify the memory device.

Williams et al. (U.S. Patent No. 5,982,697) discloses a memory devices which are initially programmed to have a desired control operation feature and which are reprogrammed during normal operation.

Sredanovic et al. (U.S. Patent No. 6,084,803) discloses the initialization operation is performed when an appropriate signal is received on one or more terminals.

Dulong (U.S. Patent No. 6,484,255 B1) discloses an initialization value is determined that identifies the potential storage location for the currently selected data element of the packed data operand to be selectively written.

Ryan et al. (U.S. Patent No. 6,535,450 B1) discloses the identification value assigned to each memory device is stored in an ID register which may be located at the associated memory device.

Contact Information

Any inquiry concerning this communication from the examiner should be directed to Dang Nguyen, who can be reached by telephone at (703) 305-1673. Normal contact times are M-F, 8-4:30.

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Upon an unsuccessful attempt to contact the examiner, the examiner's supervisor, Richard Elms, may be reached at (571) 272-1869.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist, whose telephone number is (703) 305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

Or faxed to:(703) 746-7239 (for formal communications intended for entry)

or:

(703) 746-7239 (for after-final communications)

Hand-delivered responses should be brought to

Crystal Park II, 2121 Crystal Drive

Arlington, VA, Fourth Floor (receptionist).

Dang Nguyen 7/6/2004

Vu A. Le Primary Examiner